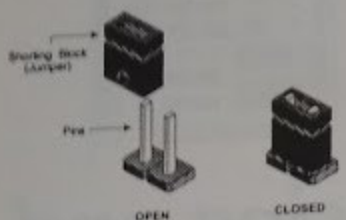
Setting Jumpers

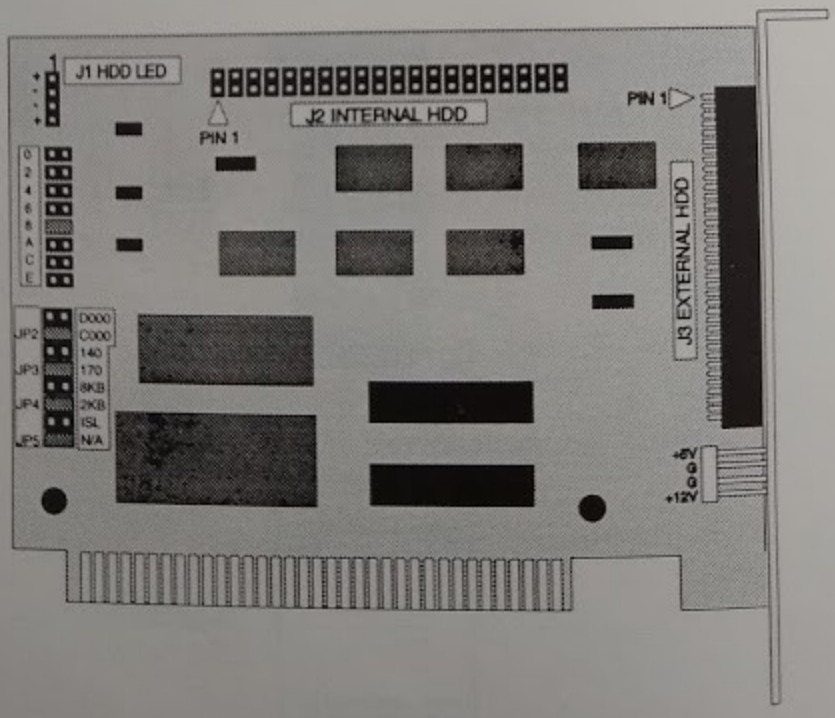
Jumpers are shorting blocks that connect two points together mechanically and electrically. When a jumper is installed, it is said to be "Closed." If a jumper is not installed, it is said to be "Open."



⚠ To keep from losing your jumpers, place unused jumpers over only one pin. This will be considered an "Open" configuration, but your jumper will be there if you need it later.

For most applications, the default jumper settings of the MCT-IDE-8 should not be changed. If the default address causes a conflict with other cards installed in your system, you may need to change the default settings. The MCT-IDE-8 has the following default address settings:

I/O Base Address 170h to 17Fh
ROM BIOS Address C800h to C9FFh



Use the following table to note the ports and addresses that may be already used by your system. Refer to your system documentation or use a diagnostic program (e.g., QAPlus by DiagSoft) to determine these ports and addresses.

	CARD 1	CARD 2	CARD 3	CARD 4	CARD 5
Adaptor					MCT-IDE-8
I/O Base Address					
ROM BIOS Address					

Configuring the MCT-IDE-8

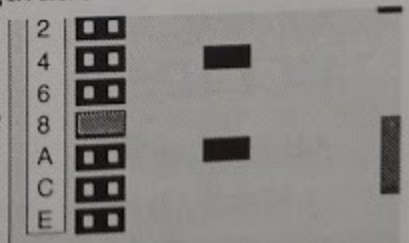
If you have determined the MCT-IDE-8 has an address conflict with one or more cards already installed in your system, use the following information to configure the card.

JP1, JP2 ROM BIOS Address

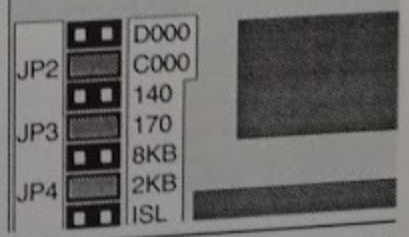
The jumpers at these locations determine what address the ROM BIOS will reside. These jumpers are located on the left side of the card. The default ROM BIOS address is ***C800***. The table at the left lists the possible ROM BIOS address and jumper configurations.

ROM BIOS Address	JP1	JP2
C000h - C1FFh	0	C000
C200h - C3FFh	2	C000
C400h - C5FFh	4	C000
C600h - C7FFh	6	C000
C800h - C9FFh	**8**	**C000**
CA00h - CBFFh	A	C000
CC00h - CDFFh	C	C000
CE00h - CFFFh	E	C000
D000h - D1FFh	0	D000
D200h - D3FFh	2	D000
D400h - D5FFh	4	D000
D600h - D7FFh	6	D000
D800h - D9FFh	8	D000
DA00h - DBFFh	A	D000
DC00h - DDFFh	C	D000
DE00h - DFFFh	E	D000

JP1 on Jumper 8 (Default)



JP2 on C000 (Default)



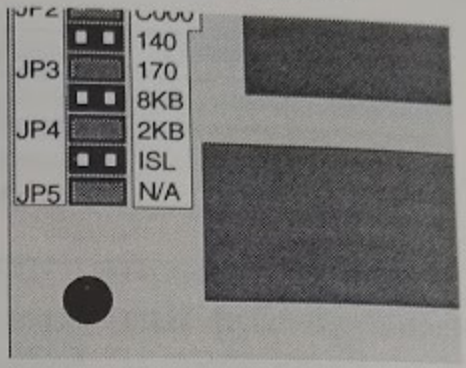
JP3, JP5 I/O Base Address

The configuration of these jumpers determines the base address of the Input/ Output Port. The default I/O base address is 170. Refer to the table on the top of page 1-3 to determine if a conflict exists with the default I/O base address.

I/O Address	JP3	JP5
170h to 17Fh	170	N/A
140h to 14Fh	140	ISL

JP3 on 170 (Default)

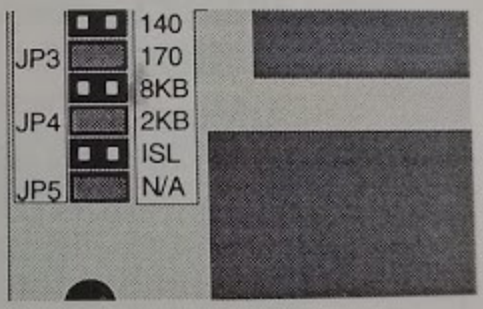
JP5 on N/A (Default)



JP4 Do Not Change

This jumper is used during factory testing and should not be removed from its default setting. The jumper should be installed on "***2KB***".

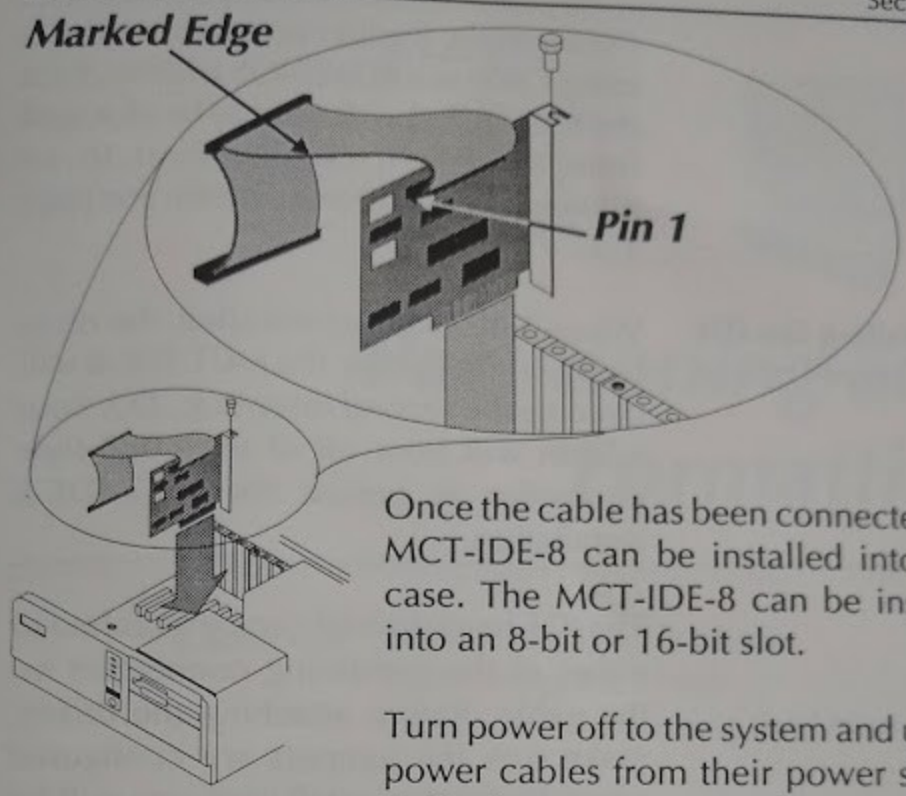
JP4 on 2KB (Default)



Installing the MCT-IDE-8

Attaching the Hard Drive Cable

The forty-pin ribbon cable supplied has one edge marked indicating pin one. When connecting the cable, be sure to connect pin one of the cable to pin one of the connector J2. See the following illustration.



Once the cable has been connected, the MCT-IDE-8 can be installed into your case. The MCT-IDE-8 can be installed into an 8-bit or 16-bit slot.

Turn power off to the system and unplug power cables from their power source. Following your case's documentation, open the case.

Holding the ribbon cable with one hand, insert the MCT-IDE-8 into the bus slot making sure that it seats firmly. You may need to use a slight rocking motion if the card seats tightly. Secure the card's bracket to case using the retaining screw from the case.

Installing the MCT-IDE-8 with an Existing Hard Drive Controller

The MCT-IDE-8 can co-exist with another 8088-style hard drive controller (i.e., an 8-bit MFM or RLL controller). The addresses the controllers use cannot be the same. It is important you determine the address your current controller is using before installing the MCT-IDE-8.